Course Information Form

This Course Information Form provides the definitive record of the designated course

Section A: General Course Information

Course Title	MSc Electronic Engineering
Final Award	MSc
Route Code	MSELEAAF
Intermediate Qualification(s)	
FHEQ Level	7
Location of Delivery	University Square Campus, Luton
Mode(s) and length of study	Full time, one year
Standard intake points (months)	October, November, February, April, June or August
External Reference Points as applicable including Subject Benchmark	QAA Characteristics statement - Master's Degrees (2020) QAA Subject Benchmark Statement - Engineering (2019) SEEC Credit Level Descriptors (2016) QAA FHEQ Level Descriptors (2014)

Professional, Statutory or Regulatory Body (PSRB) accreditation or endorsement	The Institution of Engineering and Technology - The IET This course is accredited by the IET (Institution of Engineering and Technology) accreditation at level Partial CEng (Further Learning).
HECoS code(s)	100165
UCAS Course Code	NA

Course Aims	This course provides the academic and technical skills to analyse, synthesise, interpret and make sense of modern electronic systems. It was designed with industry experts for graduates with a background in electronics, ideal for those seeking specialist careers in digital electronics or communications. You will gain advanced theoretical and practical knowledge and skills in digital communications, signal processing, electronic circuits and microprocessors, as well as an understanding of engineering best practice and how to apply it in real-life scenarios. This course is ideal if you are looking to work within the areas relating to digital electronics or communications and will prepare you for a range of specialist career options. It will enable you to meet the demands of tomorrow s engineering society. This course provides an opportunity to study the subject of electronic engineering at an advanced level. It is designed to introduce you to the fundamental principles that underpin the subject (e.g. digital signal processing, digital communications, etc.) as well as providing an insight into the fast changing nature of the subject. During the course you will get to study topics with the area of embedded systems, wireless sensor networks, optical communications and other exciting emerging technologies within the field of electronics. The course itself is designed for those who have a previous background in electronics (e.g. those who may have previously studied electrical engineering or computer science and electrical engineering). The course has been designed in conjunction with industry experts and will involve a variety of learning approaches including hands on laboratory sessions in which you will be able

Course Learning Outcomes	uld meet the appropriate learning outcomes for your award shown in the table below		
		Outcome	**Award**
	1	Demonstrate a deep and systematic understanding of electronic engineering including current and emerging theoretical and methodological approaches at various levels of abstraction.	MSc Electronic Engineering (all routes)
	2	Develop critical responses to existing theoretical methodologies and practices in electronic engineering and suggest new innovative solutions to a variety of complex electronic products.	MSc Electronic Engineering (all routes)
	3		

	9	Design and undertake a substantial investigation to address significant areas of theory and/or practice in the area of Electronic engineering, selecting appropriate methodological processes and critically evaluating their effectiveness.	MSc Electronic Engineering (all routes)
Teaching, learning and assessment strategies	A wide variety of teaching styles will be used throughout this course. The most important aspect will be that of a student centred approach. You will be encouraged (through relevant guidance) to become independent thinkers who can take responsibility for your own learning and ensure to adapt to a wide variety of different situations. The course will mainly make use of traditional lectures and practical sessions and will encourage you to engage in various scenarios (e.g. project managing, team working etc.). In addition, some units will use podcasts to provide you with an overview/summary of different topics. Some units (e.g. Research methodologies and Project Management) will include seminar based sessions and class discussions. The subject of electronic engineering involves the development of enhanced knowledge and understanding of technical concepts combined with the ability to apply these concepts with practical real world environments. With this in mind an assessment strategy that makes use of the most appropriate assessment technique (be that a written report, research essay, practical laboratory exercise, practical log book, presentation etc.) has been chosen. The assessment methods for each unit on the course have been chosen as the best way for you to demonstrate that you have acquired the requisite technical knowledge and that you can apply this knowledge within a variety of real world context. In most units, this will involve the completion of a practical based piece of work (typically a coursework assignment with a practical element) and a more formal assessment (typically a formal examination). Further information regarding the assessments used within a unit can be found within the relevant Unit Information Form for the unit.		
Learning support	The University s comprehensive student support service includes: Student Information Desk, a one-stop shop for any initial enquiries; Student Support team advising and supporting those with physical or learning needs or more general student well being; Study Hub team providing academic skills guidance; Personal Academic Tutoring system; a student managed Peer-Assisted Learning scheme; support from your lecturers		
Admissions Criteria	https://www.beds.ac.uk/entryrequirements **Approved Variations and Additions to Standard Admission** N/A		
	https://www.beds.ac.uk/about-us/our-university/academic-information		

Assessment Regulations	**Note: Be aware that our regulations change every year** **Approved Variations and Additions to Standard Assessment Regulations** N/A

Section B: Course Structure

Section C: Assessment Plan

The course is assessed as follows :

MSELEAAF- MSc Electronic Engineering

Unit Code	**Level**	**Period**	**Core/Option**	**Ass 1 Type code**	**Ass 1 Submit wk**	**Ass 2 Type code**	**Ass 2 Submit wk**	**Ass 3 Type code**	**Ass 3 Submit wk**	Ass 4 Type code	Ass 4 Submit wk
CIS117-6	7	BLK1/ BLK5/ BLK3	Core	CW-PORT	6						
CIS116-6	7	BLK2/ BLK6/ BLK4									

WR-I	Coursework - Individual Report
WR-PO	Coursework - Poster

Administrative Information

School	School of Computer Science and Technology
Head of School/Department	Paul Sant
Course Coordinator	Vladan Velisavljevic